**Lecture10 Assignments**

1. What are the advantages and disadvantages of supporting **memory-mapped I/O** to device control registers?
2. Describe **I/O Polling**. Why is I/O polling inefficient? Suggests solution(s) to overcome the inefficiency of the I/O polling operation.
3. Briefly describe **Direct Memory Access** (DMA) I/O operation. Point out the advantages of the DMA operation over **memory mapped** and **direct I/O** operations.
4. Describe various circumstances under which **nonblocking I/O** should be used. Why not just implement nonblocking I/O and have processes busy-wait until their devices are ready?
5. What are the advantages and disadvantages of supporting **memory-mapped I/O** to device control registers?
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